



C. U. SHAH UNIVERSITY, Wadhwan City

Name of Programme: M.Tech

Name of Branch: VLSI & Embedded System Design

Semester: I

w. e. f. June 2014

Sr. No	Branch Code	Subject Code	Subject Name	Teaching Schemes (Hours)				Credits	Evaluation Schemes							
				Th	Tu	Pr	Total		Theory				Practical (Marks)		Total	
									Sessional Exam		University Exam		Pr	TW		Pr
									Marks	Hours	Marks	Hours				
1	05	5TE01CVD1	CMOS VLSI Design (CVD)	03	00	02	05	04	30	1.5	70	3.0	---	20	30	150
2	05	5TE01ENC1	Embedded Network And Controllors (ENC)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
3	05	5TE01EMD1	Embedded System Design (EMD)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
4	05	5TE01HDL1	Hardware Descriptive Language (HDL)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
5	05	5TE01XXX1	Elective – I	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
			Total	19	00	10	29	24	150	---	350	---	---	100	150	750

Elective-I:

Subject Code	Subject Name
5TE01ADS1	Advanced Digital System Design (ADS)
5TE01DVD1	Digital VLSI Design (DVD)
5TE01MOS1	Modern Operating Systems (MOS)
5TE01AMA1	Advanced Microprocessors Architecture (AMA)

Th: Theory, To: Tutorial/Seminar/Field work, Pr: Practical, TW: Term Work

Note: Theory _____ 1hr = 1 Credit,

Tutorial/Practical _____ 2hr = 1 Credit,